

## RESPONSE UNDER 37 CFR 1.116 -EXPEDITED PROCEDUREEXAMINING GROUP ART UNIT 2195

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Ueda et al.

Examiner: K. Tang

Serial No:

09/682,188

Group Art Unit: 2195

Filed:

August 2, 2001

Docket: JP920000046US1 (8728-728)

For:

Sir:

MULTIPROCESSOR SYSTEM, PROCESSOR MODULE

FOR USE THEREIN, AND TASK ALLOCATION

METHOD IN MULTIPROCESSING

Mail Stop AF Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

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Do not enter -7/14/05 AMENDMENT UNDER 37 C.F.R. § 1.116

This is Amendment to the Final Office Action mailed April 22, 2005.

## CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to: Mail Stop AF, Commissioner for Patents, PO Box 1450, Alexandria VA, 22313-1450 on <u>June 22, 2005</u>.

Dated: 6-22-05

Koon Wong